



CSTIC 2025 Call for Papers

We invite you to submit your papers to CSTIC 2025, one of the largest and the most comprehensive annual semiconductor technology conferences in China. Conference of Science & Technology for Integrated Circuits (CSTIC) is organized by SEMI and IEEE. It will cover all the aspects of semiconductor technology and manufacturing, including devices, design, lithography, integration, materials, processes, manufacturing as well as emerging semiconductor technologies and silicon material applications.

****Accepted papers will be submitted for inclusion into IEEE Xplore subject to meeting IEEE Xplore's scope and quality requirements.**

Topics to be addressed at CSTIC 2025 include, but are not limited to, the following:

[Symposium I: Device Engineering and Memory Technology](#)

[Symposium II: Lithography and Patterning](#)

[Symposium III: Dry & Wet Etch and Cleaning](#)

[Symposium IV: Thin Film, Plating and Process Integration](#)

[Symposium V: CMP and Post-CMP Cleaning](#)

[Symposium VI: Metrology, Reliability and Testing](#)

[Symposium VII: Packaging and Assembly](#)

[Symposium VIII: MEMS, Sensors and Emerging Semiconductor Technologies](#)

[Symposium IX: Design and Automation of Circuits and Systems](#)

[Symposium X: AI & IC Manufacturing](#)

AWARDS

CSTIC is pleased to present three awards during the conference to recognize outstanding scientific and/or engineering work in fundamental or applied semiconductor technologies by any authors in the world:

Best Student Paper Awards

Best Young Engineer Paper Awards

Best Poster Awards



IMPORTANT DATES

Abstract Due:	Sep. 30, 2024	Copyright Form Due:	Feb. 20, 2025
Author Notification:	Nov. 15, 2024	Presentation Due:	Feb. 20, 2025
Manuscript Due:	Dec. 15, 2024	Conference Date:	Mar. 23-24, 2025

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